

RF/PULSE TRANSFORMERS

- 1 Designed for use in 50Ω wideband R.F. and fast rise time pulse applications.
- 1 Low leakage inductance, winding capacitance, and DC resistance.
- 1 TEMPERATURE RANGE:
 Operating: 0° to +70°C
 Storage: -25° to +105°C

ELECTRICAL SPECIFICATIONS @25 °C

Model ^(1) 2) Number	Turns Ratio ± 5%	Schematic Figure	Primary Inductance		Rise Time (Max.) ns	Leakage Inductance (Max.) μ H	Cw/w (Max.) pf	D.C.R. (Max.) ohms		Bandwidth For-3dB Loss		Primary ET- Constant V-μ s (Min.)
			μ H (Min.)	Pulse Width μ s For 50% Droop				N1	N2	Low Freq. MHz	High Freq. MHz	
UT10245	1:1	A	80	2.20	2.20	0.15	12	0.20	0.20	0.05	110	2.5
UT10246	1ct:1ct	C	80	2.20	3.00	0.18	15	0.20	0.20	0.05	90	2.5
UT10247	1:1ct	B	80	2.20	3.00	0.18	15	0.20	0.20	0.05	90	2.5
UT10248	1:1:1	D	40	1.10	2.00	0.10	12	0.16	0.16	0.10	150	2.0
UT10249	1:2	A	40	1.10	3.00	0.14	15	0.20	0.30	0.10	110	2.0
UT10250	1:2ct	B	40	1.10	3.00	0.14	15	0.20	0.30	0.10	110	2.0
UT10251	1:√2	A	80	2.20	4.00	0.30	18	0.20	0.30	0.05	50	2.5
UT10252	1:√2ct	B	80	2.20	3.00	0.20	18	0.20	0.30	0.05	80	2.5
UT10253	1:4	A	20	0.55	6.00	0.10	10	0.20	0.60	0.20	60	1.25
UT10254	1:4ct	B	20	0.55	6.00	0.10	10	0.20	0.60	0.20	60	1.25

1) Package Style: DIL: Dual-in-line, Package

SMD: Surface Mount Package,

LSMD: Large Row-space Surface Mount Package,

FP: Flat Package (Surface Mount)

2) Model No. Description: UT10XXX DIL Package

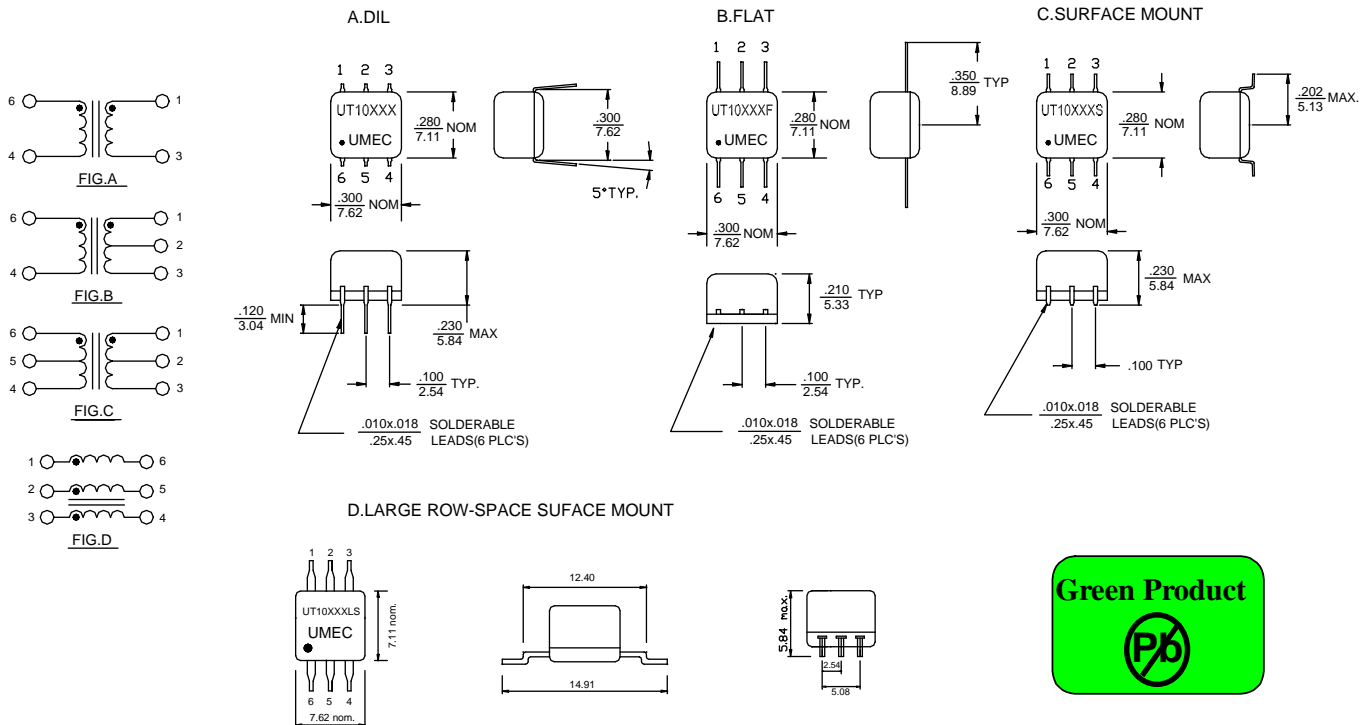
UT10XXXS SMD Package

UT10XXXLS LSMD Package

UT10XXXF FP Package

3) For RoHS compliant products, a.) the Ordering Code: TG-Model No. ex. TG-UT10245

b.) SMD Solder : Sn/Ag/Cu ; THT Solder : Sn/Cu c.) Date Code suffix to "G" (xxxxG)



Specifications are subject to change without prior notice.

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