COMMON MODE CHOKE)*

2-fold chokes for U-interfaces

electrical specifications @ 25^{0}C:

UMEC model no.	L_N mH	I_N mA	L_S uH Max.	R_{CU} Ω Nom.	U_P KVrms	figure/ schematic
2-fold current compensated choke flat design						
UT21107	2x1.7	600	2.0	0.25	1.0	A
UT21173	2x12.5	350	1.5	1.2	0.5	A
UT21161	2x28	250	1.0	0.65	0.5	A
UT21175	2x50	250	1.5	1.1	0.5	A
UT21176	2x70	200	1.0	2.1	0.5	A
upright design						
UT21507	2x1.7	600	2.0	0.25	1.0	A
UT21513	2x12.5	350	1.5	1.2	0.5	A
UT21518	2x28	250	1.0	0.65	0.5	A
UT21547	2x50	250	1.5	1.1	0.5	A
UT21522	2x70	200	1.0	2.1	0.5	A
SMT design						
UT21107-TS	2x1.7	600	2.0	0.25	1.0	A
UT21173-TS	2x12.5	350	1.5	1.2	0.5	A
UT21161-TS	2x28	250	1.0	0.65	0.5	A
UT21175-TS	2x50	250	1.5	1.1	0.5	A
UT21176-TS	2x70	200	1.0	2.1	0.5	A

*)Low cost design solution are available.

*Specifications are subject to change without prior notice.

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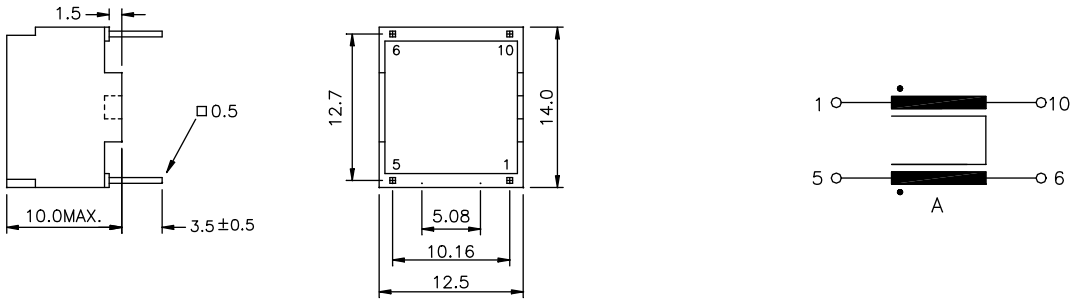
COMMON MODE CHOKE

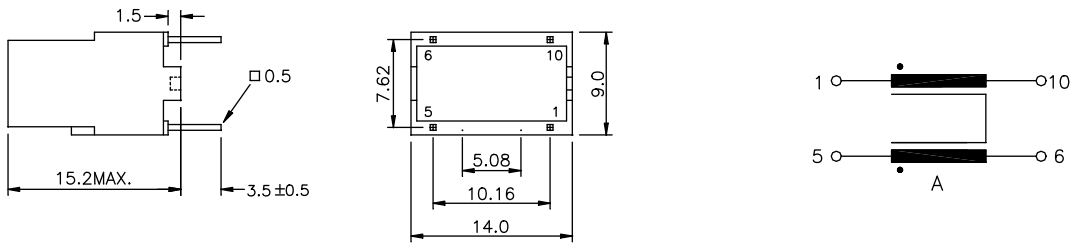
2-fold chokes for U-interfaces

Dimensions and connections(tolerance=±0.2mm)

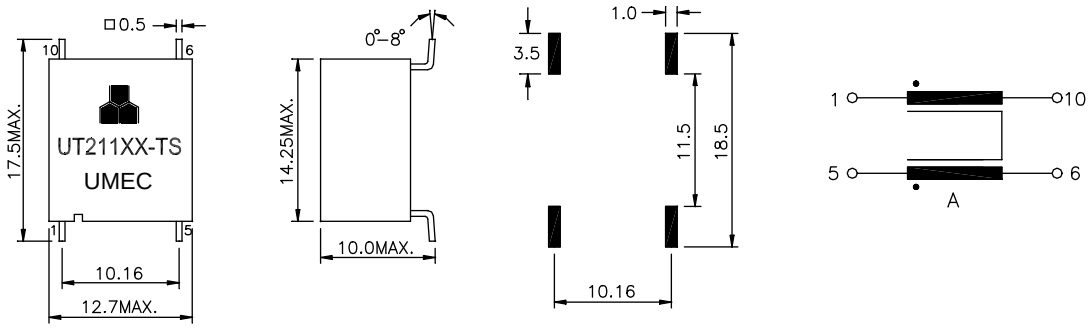
UT211..



UT215..



UT211.. –TS(SMT design)



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2-fold chokes for U-interfaces

definition of symbols:

L_N= rated inductance of a winding(tol. +50%/-30%, f=10KHz U=100mV$_{rms}$).

I_N= permissible rated current of winding.

L_S= leakage inductance of winding when all other windings short circuited(nominal value, f=100KHz U=100mV$_{rms}$).

R_{CU}= DC resistance of each winding(nominal value).

U_P=test voltage, rms value 50/60Hz, 2seconds, winding to winding.

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