

VDSL Transformers

- Developed for use in TI TNETD8000 VDSL chip set.
- Excellent Return Loss and Longitudinal Balance.
- Models with 1500Vrms isolation and 3000Vrms supplementary insulation according to IEC950, UL1950 and EN60950.

ELECTRICAL SPECIFICATIONS @25°C

Model No.	Turns Ratio (±2%)	OCL Primary (μ H Min.)	Return Loss (dB Min.) Line ref. To 100 ohms	Insertion Loss (dB Max.)	Longitudinal Balance (dB)	CM Rejection (dB Min.)	Isolation Voltage (Vrms Min.)
UT20035	1CT:1CT	190	300KHz to 1MHz : 18 1MHz to 11MHz: 20 11MHz to 20MHz: 16	200KHz to 1MHz: 0.8 1MHz to 30MHz: 0.7	200KHz: 60(Min.) 11MHz: 42(Min.) 100MHz: 30(Typ.)	10MHz: 35 30MHz: 33 100MHz: 25	3000
UT20036S	1CT:1CT	190	300KHz to 20MHz: 20 20MHz to 30MHz: 18	200KHz to 400KHz: 0.75 400KHz to 30MHz: 0.50	200KHz: 60(Min.) 11MHz: 50(Min.) 100MHz: 30(Typ.)	10MHz: 35 30MHz: 33 100MHz: 30	1500

Note: For RoHS compliant products:

1. THT type: a.) The UMEC ordering code: **TG-UT20035**

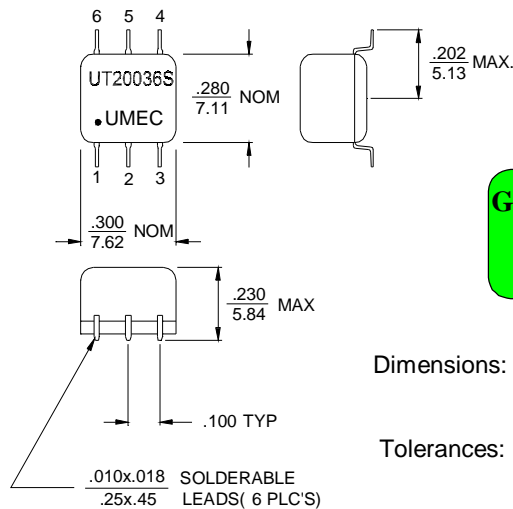
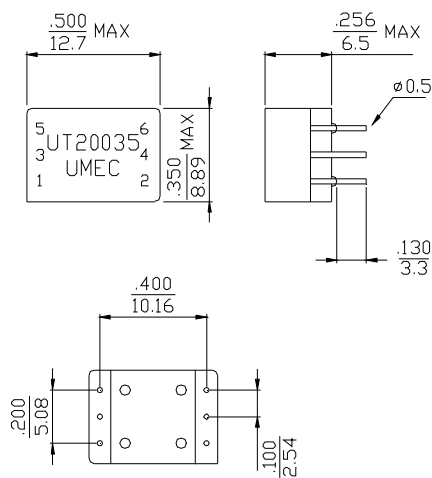
b.) Solder: Sn/Cu.

SMD type: a.) The UMEC ordering code: **TG-UT20036S**

b.) Solder: Sn/Ag/Cu

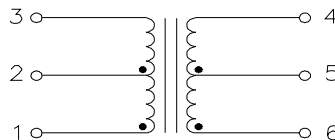
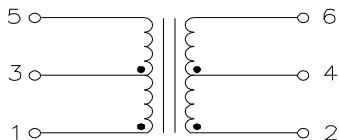
2. Date Code suffix to "G" (xxxxG).

MECHANICALS & SCHEMATICS



Dimensions: $\frac{.010}{0.25}$

Tolerances: $\frac{\text{inches}}{\text{mm}}$



DATA SHEET 14-20 FEB./06
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