

G.SHDSL

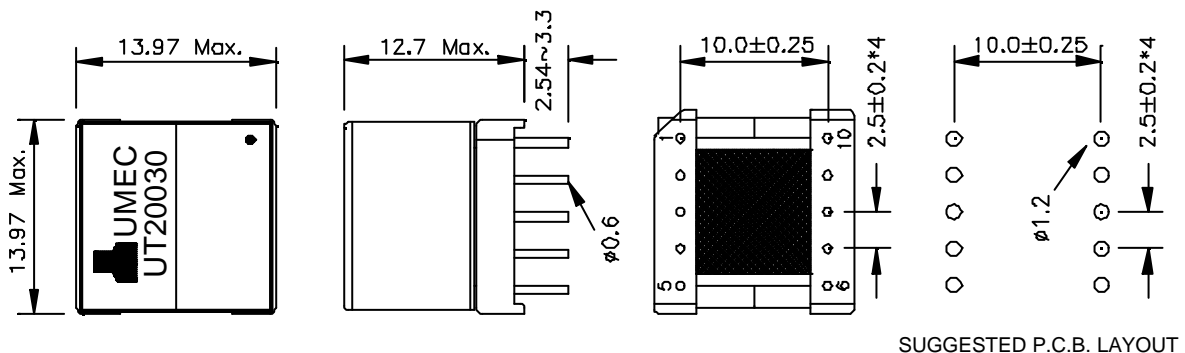
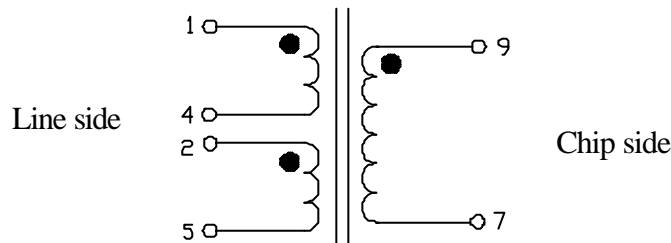
UM MODEL NO.:	SPECIFICATIONS	REV.	
UT20030	G.SHDSL Transformer	B0	2002/40

Electrical specification at 25⁰C:

- 1.) Turns Ratio and Polarity : 2.0CS : 1±2% , Line Side to Chip Side .
- 2.) D.C.R : 2.43Ω ±10% , PIN(1-4) .
: 2.43Ω ±10% , PIN(2-5) .
: 1.46Ω ±10% , PIN(9-7).
- 3.) Line side Inductance : 2.0mH±6% , at 10KHz 100mV, PIN(1-5) ; PIN(4-2) shorted .
- 4.) D.C. current : 160mADC max.
- 5.) Leakage Inductance : 30uH max. , at 100KHz 20mV, PIN(1-5) ; PIN(4-2)&PIN(9-7) shorted.
- 6.) Insertion Loss : 0.5dB max. , 40KHz .
- 7.) Return Loss : 16.0dB min. , 40KHz to 300KHz .
- 8.) Longitudinal Balance : 50dB min. , 30KHz to 300KHz .
- 9.) Total Harmonic Distortion : -60dB typ. , at 1KHz, 2Vrms .
- 10.) HI-pot test : 2000Vac 60s or 2050Vac 2s , Line side to Chip side .
- 11.) Operation Temperature : -40⁰C to +85⁰C

Note: the transformer meets the specifications for supplementary insulation per IEC 950 with working voltage of 250V.

Schematic diagram & Dimension:



NOTE: Specifications are subject to change without prior notice.

UNIT : mm

Tolerances : ±0.2mm

