

# VDSL

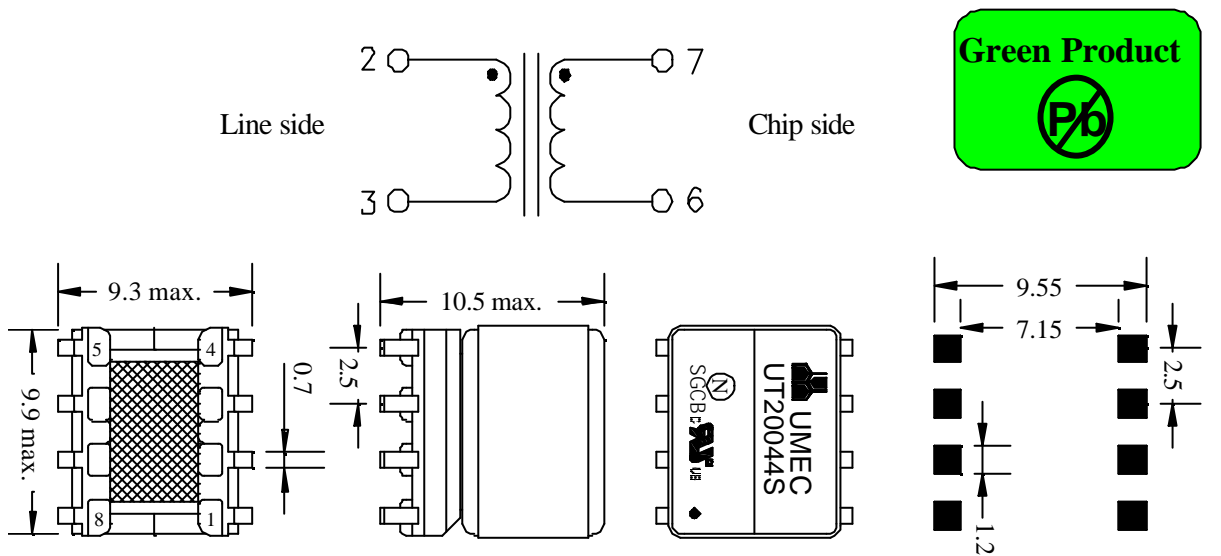
UM MODEL NO.:	SPECIFICATIONS	REV.	
UT20044S	VDSL Transformer for Ikanos SmartLeap chipset	A0	2005/47

### Electrical specification at 25°C:

- 1.) Turns Ratio and Polarity : 3.5 : 1 ±2% , Line side to Chip side.
- 2.) D.C.R. : 1.5Ω max. , PIN(2-3) .  
: 0.3Ω max. , PIN(7-6) .
- 3.) Line side Inductance : 200uH min. , at 10KHz 100mV, PIN(2-3)
- 4.) Leakage Inductance : 1.0uH max. ,at 100KHz 100mV, PIN(2-3) ; PIN(7-6) shorted
- 5.) Insertion Loss : 1.3dB max. ,at 80KHz~12.5MHz
- 6.) HI-pot test : 1500Vac 60s or 1875Vac 1s , Line side to Chip side .

**Note: The SGCB series transformer meets the specifications for supplementary insulation per UL60950-1, CSA60950-1, EN60950-1, IEC60950-1 with working voltage of 250V.**

### Schematic diagram & Dimension:



SUGGESTED P.C.B. LAYOUT

- NOTE :
1. Packaging Information: Tape and Reel according to Item NO. "EP7H" of data sheet 01-00
  2. For RoHS compliant products:
    - a.) The UMEC ordering code: **TG-UT20044S**
    - b.) Date Code suffix to "G" (xxxxG).
    - c.) Solder : Sn/Ag/Cu .
  3. Specifications are subject to change without prior notice.

UNIT : mm

Tolerances : ±0.2mm

### AGENCY APPROVALS

UL60950-1	: FILE NO. E212730	IEC60950-1	: CB Rrf. #DK-8600
CSA 60950-1 (via C-UL)	: FILE NO. E212730	Nordic, Japan, & Australia Deviations	: CB Rrf. #DK-8600
Nemko (EN60950-1)	: Certificate NO. P05203901		



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