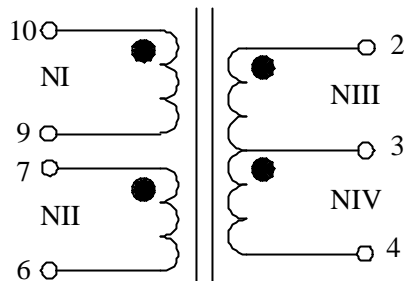


MDSL

UM MODEL NO.:	SPECIFICATION	REV.	
UT20117	MDSL Transformer for SK70725/SK70721	C1	01/42

Characteristic data:
 $R_I + R_{II} \leq 6.0\Omega$
 $R_{III} + R_{IV} \leq 3.2\Omega$
 $I_{dc} = 150\text{mA max.}$

Schematic diagram:



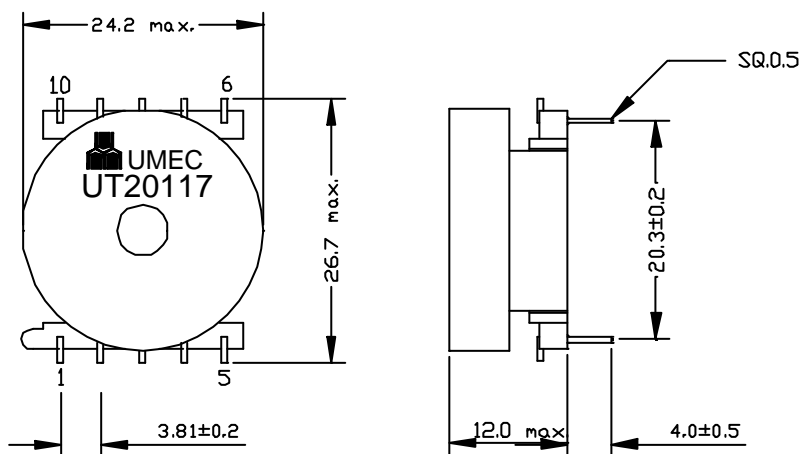
Line side : Chip side

TRUNS RATIO : 1.8:1 (NI+NII : NIII+NIV)

Electrical Specification at 25°C:

- 1.) $L_{III} + L_{IV} = 3.6\text{mH} \pm 10\%$, (NIII+NIV series), at 1KHz, 1V.
- 2.) Polarity and turns ratio tolerance: $\pm 1\%$
- 3.) $L_s \text{ III} + \text{IV} \leq 17.5\mu\text{H}$, (NIII+NIV series, NI+NII shorted), at 100KHz, 100mV.
- 4.) $C_k \leq 50\text{pF}$, (NI || NII to NIII || NIV), at 10KHz 100mV.
- 5.) Longitudinal Balance $\geq 50\text{dB}$, 5KHz to 68KHz.
- 6.) T.H.D $\leq -70\text{dB}$, at 5KHz 2V.
- 7.) HI-pot test:
 $U_p = 1.5\text{KVac}$ 2sec 0.5mA, Pri. to Sec.

Dimension:



Note: Specifications are subject to change without prior notice.

UNIT: mm

Tolerances: $\pm 0.2\text{mm}$



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