

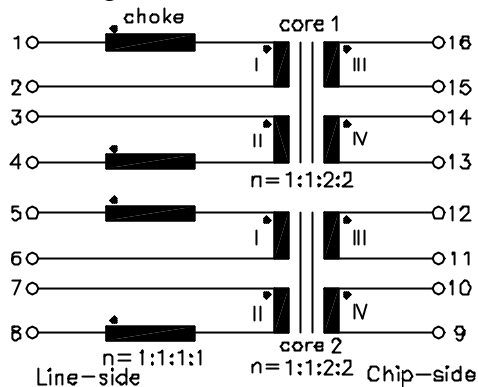
ISDN

UM MODEL NO.:	SPECIFICATION	REV.	
UT20404	S-Interface Module(with choke: 5mH)	A0	01/33

Characteristic data:

Line Side D.C.R.= $4.5 \Omega \pm 15\%$
 Chip Side D.C.R.= $4.5 \Omega \pm 15\%$

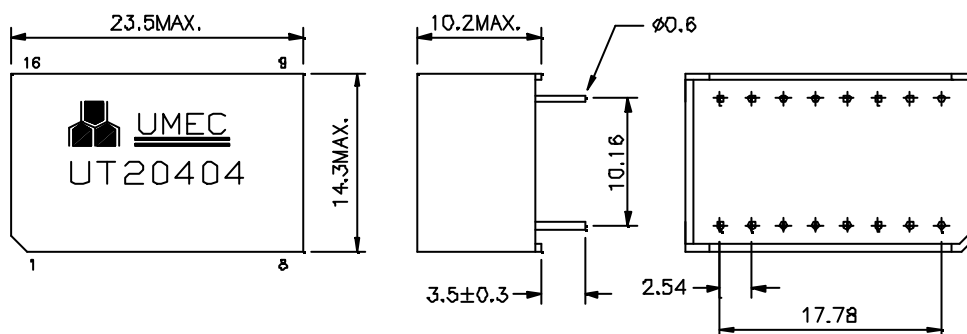
Schematic diagram:



Electrical Specification at 25°C:

- 1.) Line Side Inductance $\geq 22\text{mH}$, at 10KHz 100mV
- 2.) Polarity and turns ratio tolerance: $\pm 2\%$
- 3.) Line Side Leakage Inductance $\leq 5.0\mu\text{H}$, at 100KHz 100mV
- 4.) Interwinding Capacitance $\leq 100 \text{ pF}$, at 10KHz 100mV
- 5.) Common Choke Inductance : $5\text{mH} \pm 30\%$, at 10kHz 100mV
- 6.) HI-POT test:
 $U_p = 2.0\text{KVrms}, 2\text{s}$ (Line Side to Chip Side)

Dimensions:



Note: Specifications are subject to change without prior notice.

UNIT: mm

Tolerances: $\pm 0.2\text{mm}$

