

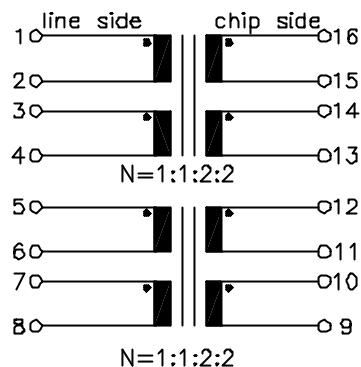
# ISDN

UM MODEL NO.:	SPECIFICATION	REV.	
UT20795-00TS	S-Interface Module	A2	02/42

**Characteristic data:**

Line Side D.C.R. =  $3.0\Omega \pm 15\%$   
 Chip Side D.C.R. =  $5.6\Omega \pm 15\%$   
 $\Delta I_{DC} = 4mA$

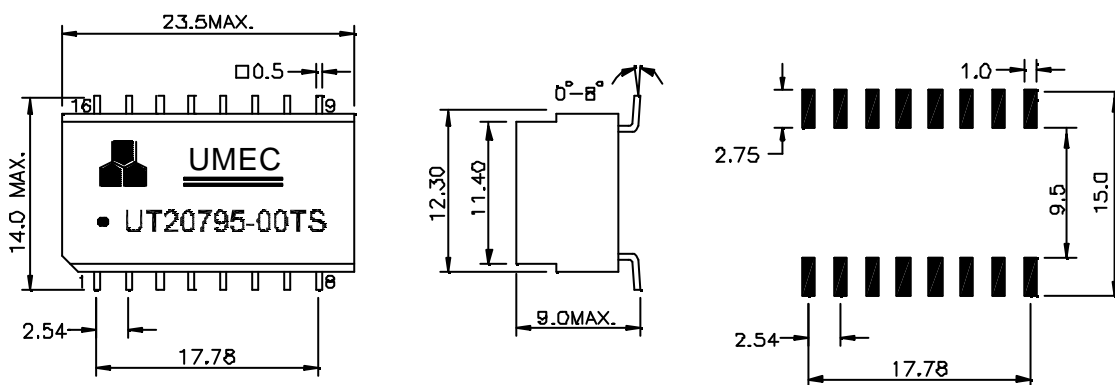
**Schematic diagram:**



**Electrical Specification at 25°C:**

- 1.) Line Side Inductance  $\geq 30mH$ , at 10KHz, 100mV
- 2.) Polarity and turns ratio tolerance:  $\pm 2\%$ ,
- 3.) Line Side Leakage Inductance  $\leq 5.0uH$ , at 100KHz, 100mV
- 4.) Interwinding Capacitance  $\leq 100pF$ , at 100KHz, 100mV
- 5.) HI-POT test:  
 $U_p = 2.0KV_{rms}, 2s$  (Line Side To Chip Side)

**Dimensions:**



NOTE: 1. PACKAGING INFORMATION-TAPE AND REEL  
 ACCORDING TO ITEM NO. "K30SS" OF DATA SHEET 01-00  
 2. Specifications are subject to change without prior notice.

UNIT: mm

Tolerances:  $\pm 0.2mm$



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E10-013-C