

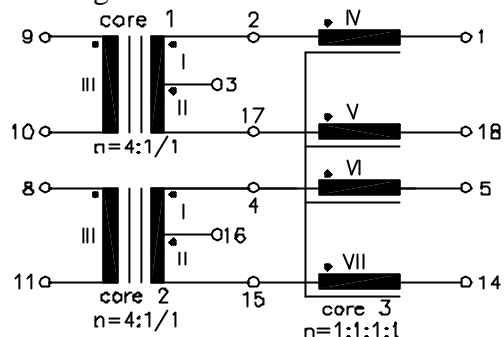
# ISDN

UM MODEL NO.:	SPECIFICATION	REV.	
UT21615-01	S <sub>O</sub> -Interface Module	A1	05/35

### Characteristic data:

$R_I = R_{II} \approx 0.6\Omega$   
 $R_{III} \approx 3.0\Omega$   
 $R_{IV} \sim R_{VII} \approx 1.1\Omega$   
 $\Delta I_{dc} = 3.6\text{mA}$   
 $T_u(\text{amb}) \leq 60^\circ\text{C}$

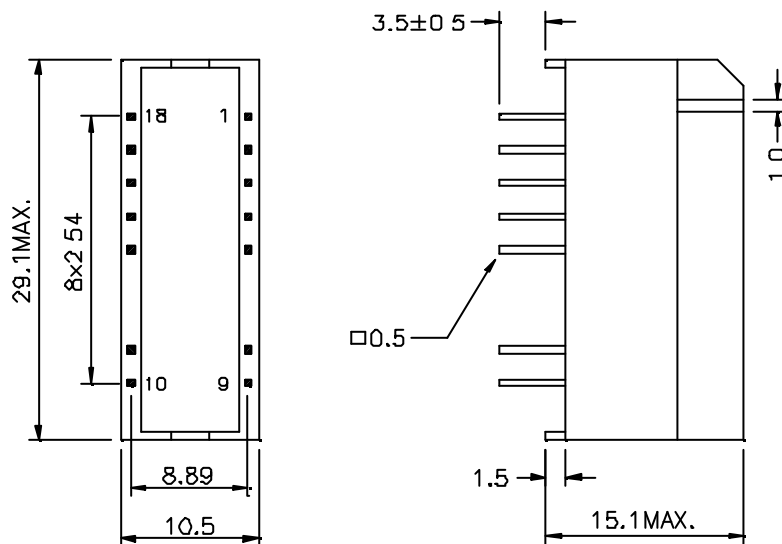
### Schematic diagram:



### Electrical Specification at 25°C:

- 1.)  $L_{I+II} \geq 30\text{mH}$ , (NI+II series), at 10KHz 100mV (core 1,2)
- 2.) Polarity and turns ratio tolerance:  $\pm 1\%$  (core 1,2,3)
- 3.)  $C_k \leq 120\text{pF}$ , (NIII to NI+NIV || NII+V, or NI+VI || NII+VII), at 10KHz 100mV (core 1,2)
- 4.)  $L_s I+II \leq 3.0\mu\text{H}$ , (NI+II series, NIII shorted), at 100KHz 100mV (core 1,2)
- 5.)  $L_s IV \leq 0.6\mu\text{H}$ , (NV, VI, VII shorted), at 100KHz 100mV (core 3)
- 6.)  $L_{IV} = L_V = L_{VI} = L_{VII} = 5\text{mH} + 50\% / -30\%$ , at 10KHz 100mV (core 3)
- 7.)  $Z_I = Z_{II} \geq 625\Omega$ , at 20KHz, 100mV with  $\Delta I_{dc} = 3.6\text{mA}$  (core 1,2)
- 8.) HI-pot test:  
 $U_p = 1.5\text{KV}_{rms}, 2\text{s}$  [ NI/II (core 1+ core 2) to NIII (core 1+ core 2) ]  
 $U_p = 0.5\text{KV}_{rms}, 2\text{s}$  [ NIV+V (core 3)+NIII (core 1) to (NVI+VII (core 3)+NIII (core 2)) ]

### Dimension:



- NOTE: 1. For RoHS compliant products:
- a.) The UMEC ordering code: **TG-UT21615-01**
  - b.) Date Code suffix to "G" (xxxxG).
  - c.) Solder : Sn/ Cu .
2. Specifications are subject to change without prior notice.

UNIT: mm

Tolerances  $\pm 0.2\text{mm}$

E10-013-C



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