

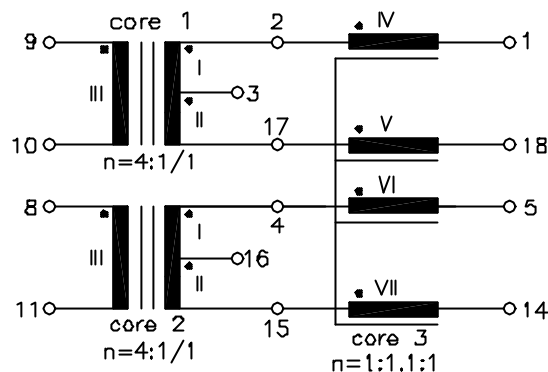
ISDN

UM MODEL NO.:	SPECIFICATION	REV.	
UT21621	S _O -Interface Module	A1	98/47

Characteristic data:

$f=96\text{KHz}$
 $C_w \text{ I+II} \approx 100\text{pF}$
 $R_I=R_{II} \approx 0.42\Omega$
 $R_{III} \approx 2.5\Omega$
 $R_{IV} \sim \text{VII} \approx 0.6\Omega$
 $\Delta I_{dc}=3\text{mA}$
 $T_u(\text{amb}) \leq 60^\circ\text{C}$

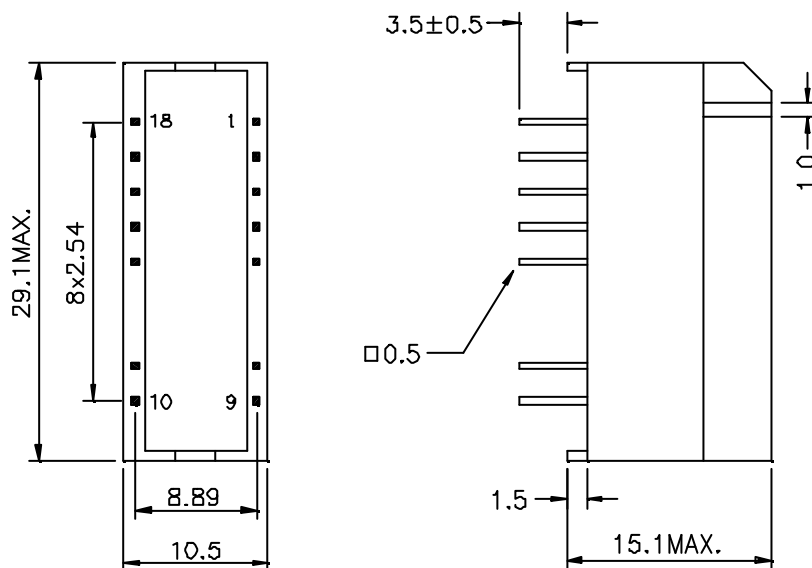
Schematic diagram:



Electrical Specification at 25⁰C:

- LI+II $\geq 30\text{mH}$, (NI+II series), at 10KHz 100mV (core 1,2)
- Polarity and turns ratio tolerance $\pm 1\%$ (core 1,2,3)
- Ck $\leq 120\text{pF}$, (NIII to NI+NIV || NII+V, or NI+VI || NII+VII), at 10KHz 100mV (core 1,2)
- Ls I+II $\leq 3.0\mu\text{H}$, (NI+II series, NIII shorted), at 100KHz 100mV (core 1,2)
- Ls IV $\leq 0.3\mu\text{H}$, (NV, VI, VII shorted), at 100KHz 100mV (core 3)
- LIV=LVI=LVII=1.7mH +50%/-30%, at 10KHz 100mV (core 3)
- ZI=ZII $\geq 625\Omega$, at 20KHz 100mV with $\Delta I_{dc}=3\text{mA}$ (core 1,2)
- HI-pot test:
 Up=1.5KVrms, 2s [NI/II (core 1+core 2) to NIII (core 1+core 2)]
 Up=0.5KVrms, 2s [NIV+V (core 3)+NIII (core 1) to NVI+VII (core 3)+NIII (core 2)]

Dimension:



NOTE: Specifications are subject to change without prior notice.

UNIT: mm

Tolerances $\pm 0.2\text{mm}$



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