

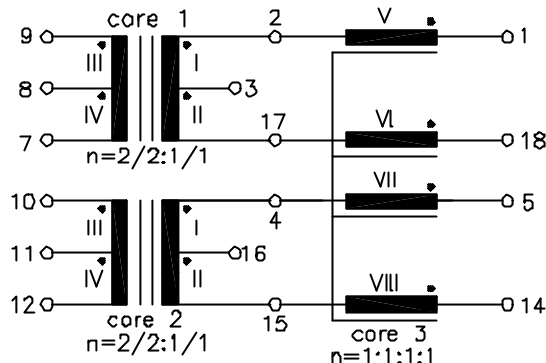
ISDN

UM MODEL NO.:	SPECIFICATION	REV.	
UT21624-TS	S _O -Interface Module	A3	06/37

Characteristic data:

$f=96\text{KHz}$
 $C_w \text{ I+II} \approx 200\text{pF}$
 $R_{\text{I}}=R_{\text{II}} \approx 0.55\Omega$
 $R_{\text{III}}=R_{\text{IV}} \approx 1.6\Omega$
 $R_{\text{V}} \sim R_{\text{VIII}} \approx 1.1\Omega$
 $\Delta I_{\text{dc}}=5\text{mA}$
 $T_{\text{u}}(\text{amb}) \leq 60^\circ\text{C}$

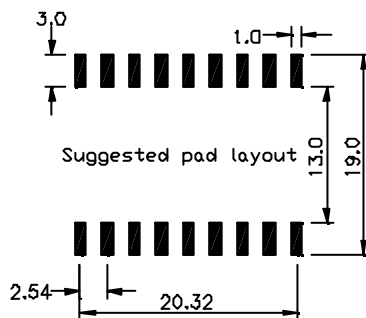
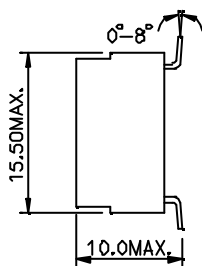
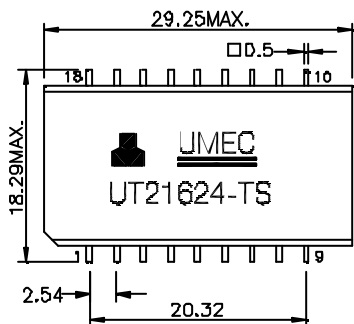
Schematic diagram:



Electrical Specification at 25⁰C:

- LI+II $\geq 30\text{mH}$, (NI+II series), at 10KHz 100mV (core 1,2)
- Polarity and turns ratio tolerance $\pm 1\%$ (core 1,2,3)
- Ck $\leq 150\text{pF}$, (NIII+IV to NI+V || NII+VI, or NI+VII || NII+VIII), at 10KHz 100mV (core 1,2)
- Ls I+II $\leq 5.0\mu\text{H}$, (NI+II series, NIII+IV shorted), at 100KHz 100mV (core 1,2)
- Ls V $\leq 0.6\mu\text{H}$, (NVI, VII, VIII shorted), at 100KHz 100mV (core 3)
- LV=LVI=LVII=LVIII=5.0mH +50%/-30%, at 10KHz 100mV (core 3)
- ZI=ZII $\geq 625\Omega$, at 20KHz 100mV with $\Delta I_{\text{dc}}=5\text{mA}$ (core 1,2)
- HI-pot test:
 $U_p=2.0\text{KVrms}, 2\text{s}$ [NI/II (core 1+core 2) to NIII/IV (core 1+core 2)]
 $U_p=0.5\text{KVrms}, 2\text{s}$ [NV+VI (core 3)+NIII/IV (core 1) to (NVII+VIII (core 3)+NIII/IV (core 2)]

Dimension:



- Note: 1. Packaging information-tape and reel according to item no. "K40S" of data sheet 01-00
 2. For RoHS compliant products:
 a.) The UMEC ordering code: **TG-UT21624-TS**
 b.) Date Code suffix to "G" (xxxxG)
 c.) Solder: Sn/Ag/Cu
 3. Specifications are subject to change without prior notice

UNIT: mm

Tolerances $\pm 0.2\text{mm}$



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