

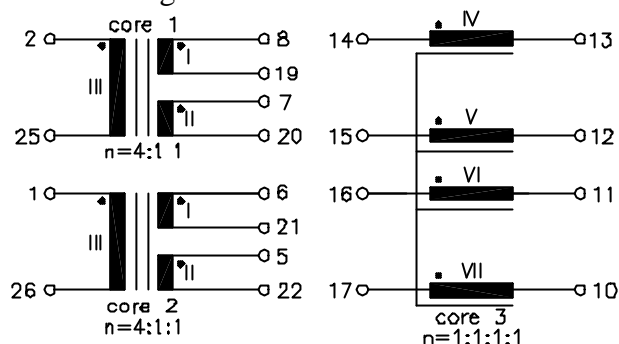
ISDN

UM MODEL NO.:	SPECIFICATION	REV.	
UT21816	S _O -Interface Module	C3	06/01

Characteristic data:

$f=96\text{KHz}$
 $C_{wI+II} \approx 30\text{pF}$
 $U_{I+II} \leq 750\text{mV}$
 $R_{I+II} \approx 0.5\Omega$
 $R_{III} \approx 3.4\Omega$
 $R_{IV} \sim R_{VII} \approx 1.1\Omega$
 $\Delta I_{dc} = 3.6\text{mA}$
 $T_{u(\text{amb})} \leq 60^{\circ}\text{C}$

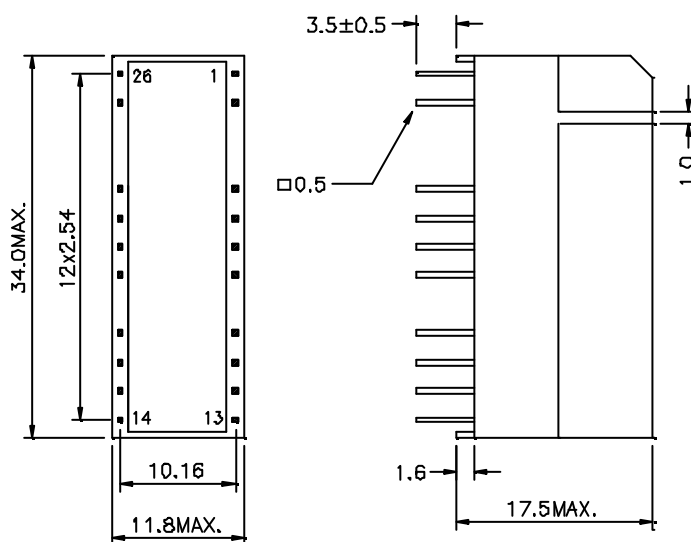
Schematic diagram:



Electrical Specification at 25⁰C:

- 1.) $L_{I+II} \geq 30\text{mH}$, (N_{I+II} series), at 10KHz 100mV (core 1,2)
- 2.) Polarity and turns ratio tolerance $\pm 1\%$ (core 1,2,3)
- 3.) $C_k \leq 45\text{pF}$, (N_{III} to $N_{I+IV} \parallel N_{II+V}$, or $N_{I+VI} \parallel N_{II+VII}$), at 10KHz 100mV (core 1,2)
- 4.) $L_s I+II \leq 5.0\mu\text{H}$, (N_{I+II} series, N_{III} shorted), at 100KHz 100mV (core 1,2)
- 5.) $L_{IV} = L_V = L_{VI} = L_{VII} = 5\text{mH} +50\%/-30\%$, at 10KHz 100mV (core 3)
- 6.) $L_s IV \leq 0.6\mu\text{H}$, (N_V, VI, VII shorted), at 100KHz 100mV (core 3)
- 7.) $Z_I = Z_{II} \geq 625\Omega$, at 20KHz 100mV with $\Delta I_{dc} = 3.6\text{mA}$ (core 1,2)
- 8.) HI-pot test:
 - $U_p = 4.0\text{KVrms}, 2\text{s}$ [N_{I+II} (core 1+core 2) + $N_{IV+V+VI+VII}$ (core 3) to N_{III} (core 1+2)]
 - $U_p = 0.5\text{KVrms}, 2\text{s}$ [N_{I+II} (core 1+core 2) to $N_{IV+V+VI+VII}$ (core 3)]
 - $U_p = 0.5\text{KVrms}, 2\text{s}$ [$N_{I+II+IV+V}$ (core 1+core 3) to $N_{I+II+VI+VII}$ (core 2+core 3)]

Dimension:



NOTE : 1. For RoHS compliant products:

- a.) The UMEC ordering code: **TG-UT21816**
- b.) Date Code suffix to "G" (xxxxG).
- c.) Solder : Sn/ Cu .

2. Specifications are subject to change without prior notice.

UNIT: mm

Tolerances $\pm 0.2\text{mm}$

E10-013-C



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