ISDN

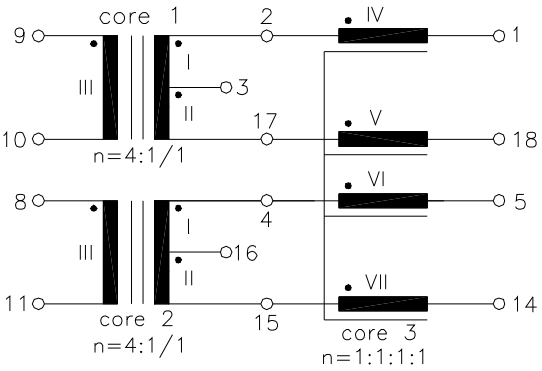
UM MODEL NO.:	SPECIFICATION	REV.	
UT28615	S_O-Interface Module	B4	99/05

Characteristic data:

RI+RII ≈ 2.4Ω($R_{CU.L}$)
RIII ≈ 5.0Ω($R_{CU.IC}$)
RIV ~ VII ≈ 1.1Ω
ΔIdc=3mA
Tu(amb)≤60^0C

***Approved by Winbond IC chip 6690 and 6692**

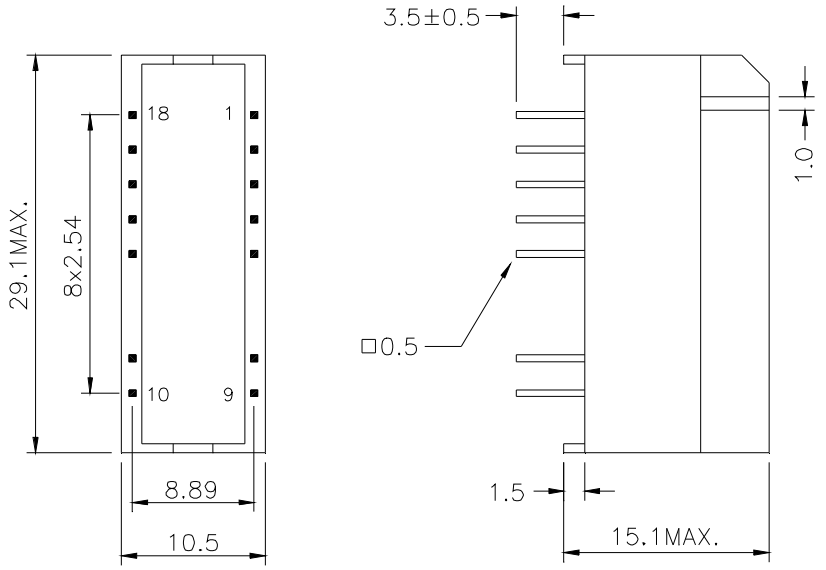
Schematic diagram:



Electrical Specification at 25^0C:

1.)LI+II≥22mH,(NI+II series), at 10KHz 100mV(core 1,2)
2.)Polarity and turns ratio tolerance: ±2%(core 1,2)
3.)Polarity and turns ratio tolerance: ±1%(core 3)
4.)Ck≤120pF,(NIII to NI+NIV ‖ NII+V, or NI+VI ‖ NII+VII), at 10KHz 100mV(core1,2)
5.)Ls I+II≤4.0uH,(NI+II series, NIII shorted), at 100KHz 100mV(core 1,2)
6.) L_SIV≤0.6uH,(NV, VI, VII shorted), at 100KHz 100mV(core 3)
7.) LIV=LV=LVI=LVII=5mH +50%/-30%, at 10KHz 100mV (core 3)
8.) ZI=ZII≥625Ω, at 20KHz 100mV with ΔIdc=3mA (core 1,2)
9.)HI-pot test:
Up=1.5KVrms,3s [NI/II(core 1+ core 2) to NIII(core 1+ core 2)]
Up=0.5KVrms,2s [NIV+V(core 3)+NIII(core 1) to (NVI+VII(core 3)+NIII(core 2)]

Dimension:



NOTE: Specifications are subject to change without prior notice.

UNIT: mm Tolerances: ±0.2mm

E10-013-C

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