

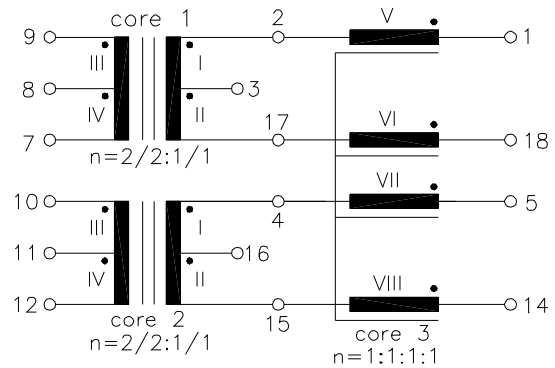
# ISDN

UM MODEL NO.:	SPECIFICATION	REV.	
UT28624-S	S <sub>O</sub> -Interface Module	C3	01/43

### Characteristic data:

$RI=RII \approx 1.45\Omega$   
 $RIII=RIV \approx 2.65\Omega$   
 $RV \sim VIII \approx 1.1\Omega$   
 $\Delta I_{dc}=3.6mA$   
 $T_u(amb) \leq 60^{\circ}C$

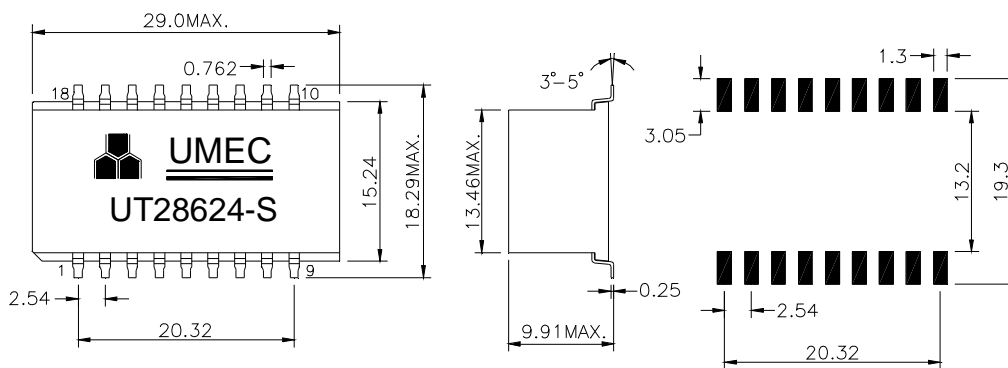
### Schematic diagram:



### Electrical Specification at 25<sup>0</sup>C:

- LI+II $\geq$ 22mH,(NI+II series), at 10KHz 100mV(core 1,2)
- Polarity and turns ratio tolerance:  $\pm 1\%$ (core 1,2,3)
- Ck $\leq$ 150pF,(NIII+IV to NI+V  $\parallel$  NII+VI, or NI+VII  $\parallel$  NII+VIII), at 10KHz 100mV(core 1,2)
- Ls I+II $\leq$ 6.0uH,(NI+II series, NIII+IV shorted), at 100KHz 100mV(core 1,2)
- Ls V $\leq$ 0.6uH,(NVI, VII, VIII shorted), at 100KHz 100mV(core 3)
- LV=LVI=LVII=LVIII=5.0mH +50%/-30%, at 10KHz 100mV(core 3)
- ZI=ZII $\geq$ 625 $\Omega$ , at 20KHz 100mV with  $\Delta I_{dc}=3.6mA$ (core 1,2)
- HI-pot test:  
 $U_p=2.0KV_{rms}, 2s$  [ NI/II (core 1+core 2) to NIII/IV (core 1+core 2) ]  
 $U_p=0.5KV_{rms}, 2s$  [ NV+VI(core 3)+NIII/IV (core 1) to (NVII+VIII(core 3)+NIII/IV (core 2) ]

### Dimension:



NOTE: Specifications are subject to change without prior notice.

UNIT: mm

Tolerances:  $\pm 0.2mm$



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