

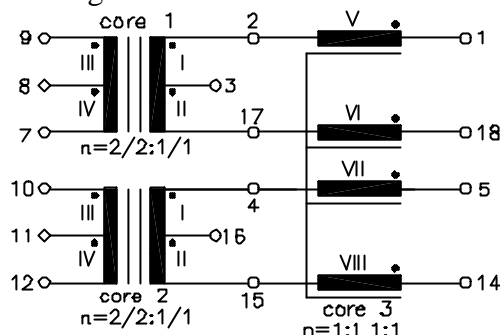
ISDN

UM MODEL NO.:	SPECIFICATION	REV.	
UT28624A	S _O -Interface Module	A4	05/35

Characteristic data:

$R_I=R_{II} \approx 0.6\Omega$
 $R_{III}=R_{IV} \approx 1.8\Omega$
 $R_V \sim R_{VIII} \approx 1.1\Omega$
 $\Delta I_{dc}=5mA$
 $T_u(amb) \leq 60^\circ C$

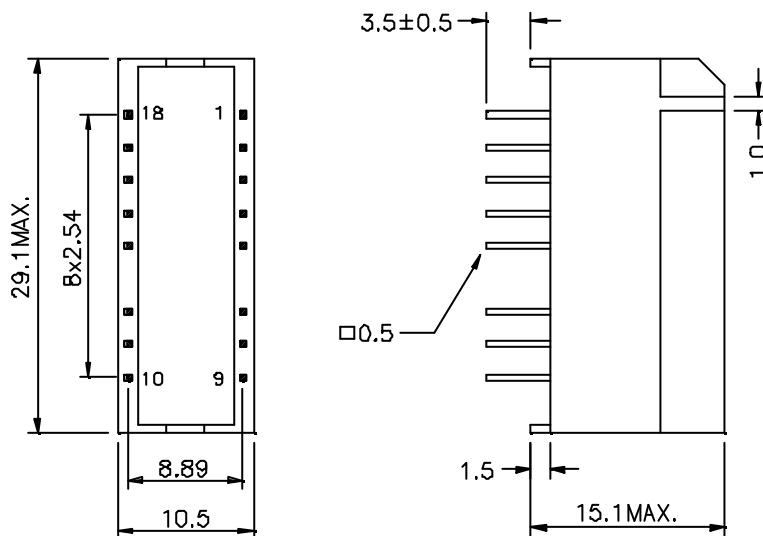
Schematic diagram:



Electrical Specification at 25⁰C:

- 1.) $L_{I+II} \geq 30mH$, (NI+II series), at 10KHz 100mV (core 1,2)
- 2.) Polarity and turns ratio tolerance: $\pm 1\%$ (core 1,2,3)
- 3.) $C_k \leq 150pF$, (NIII+IV to NI+V || NII+VI, or NI+VII || NII+VIII), at 10KHz 100mV (core 1,2)
- 4.) $L_s I+II \leq 5.0uH$, (NI+II series, NIII+IV shorted), at 100KHz 100mV (core 1,2)
- 5.) $L_s V \leq 0.6uH$, (NVI, VII, VIII shorted), at 100KHz 100mV (core 3)
- 6.) $L_V = L_{VI} = L_{VII} = L_{VIII} = 5.0mH \pm 50\% / -30\%$, at 10KHz 100mV (core 3)
- 7.) $Z_I = Z_{II} \geq 625\Omega$, at 20KHz 100mV with $\Delta I_{dc} = 5mA$ (core 1,2)
- 8.) HI-pot test:
 $U_p = 2.0KV_{rms}, 2s$ [NI/II (core 1+core 2) to NIII/IV (core 1+core 2)]
 $U_p = 0.5KV_{rms}, 2s$ [NV+VI (core 3)+NIII/IV (core 1) to (NVII+VIII (core 3)+NIII/IV (core 2))]

Dimension:



- NOTE: 1. For RoHS compliant products:
- a.) The UMEC ordering code: **TG-UT28624A**
 - b.) Date Code suffix to "G" (xxxxG).
 - c.) Solder : Sn/ Cu .
2. Specifications are subject to change without prior notice.

UNIT: mm

Tolerances $\pm 0.2mm$



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