

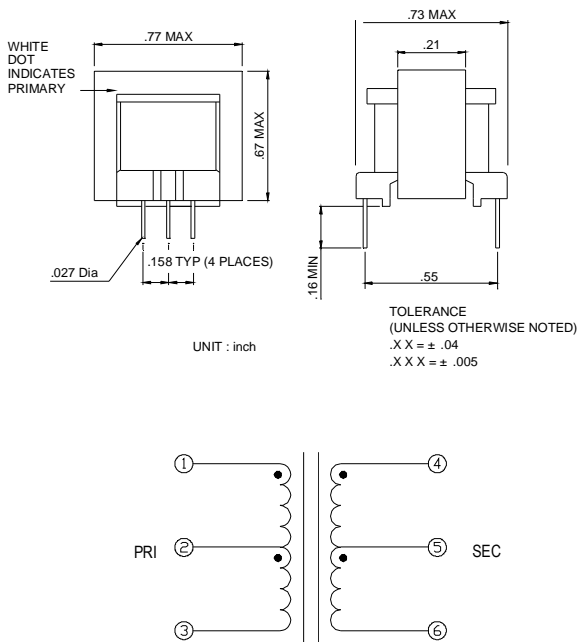
# Telephone Interconnect Transformer – UT41002

## ELECTRICAL SPECIFICATIONS AT 25°C :

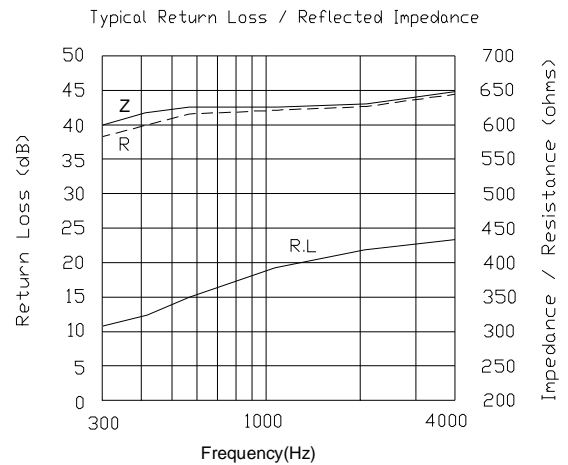
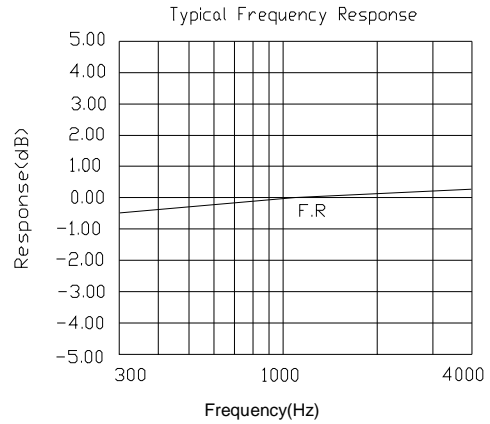
- \*Impedance : PRI.  $600\Omega \pm 10\%$  , with  $600\Omega$  load on SEC.  
@ 1KHz 1V.
- \*Turns Ratio : 1:1  $\pm 3\%$
- \*D.C.Current in PRI. : None.
- \*D.C.Resistance : PRI.:  $32\text{ohms} \pm 15\%$ .  
SEC.:  $43\text{ohms} \pm 15\%$ .
- \*Dielectric Test : 1500Vac , PRI. to SEC and core.  
500Vac , SEC. to core.
- \*Insertion Loss : 1.4dB max. , @ 1KHz, 0dBm.
- \*Frequency Response :  $\pm 0.6\text{dB}$  , @ 300Hz to 3.5KHz.
- \*Total Harmonic Distortion : 0.5% max. , @ 300Hz , 0dBm .
- \*Return Loss : 15dB min. , @ 1KHz
- \*Longitudinal Balance : 60dB min., @ 300Hz 0dBm.
- \*Wideband Response :  $\pm 1.4\text{dB}$  , @ 100Hz to 10KHz.

Note: Specifications are subject to change without prior notice.

## SCHEMATIC DIAGRAMS & DIMENSIONS :



## TYPICAL GRAPHS :



## SPICE TRANSMISSION MODEL :

